|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 外设名称 | 引脚名称 | | FPGA引脚 |  |  |
| JTAG/USB | UART\_RXD | | Y21 |  |  |
|  | UART\_TXD | | Y22 |  |  |
| 复位键 | rst\_n | | F4 |  |  |
| 时钟 | FPGA\_GCLK\_50M | | W19 | 50MHz |  |
|  | FPGA\_CLK\_100M | | H4 | 100MHz |  |
|  | FPGA\_CLK\_3Hz | | Y18 | 3Hz |  |
| 蜂鸣器 | BUZZER | | P20 |  |  |
| LED/DAC | LED0 | DAC\_D0 | AA21 |  |  |
|  | LED1 | DAC\_D1 | AA20 |  |  |
|  | LED2 | DAC\_D2 | W22 |  |  |
|  | LED3 | DAC\_D3 | W21 |  |  |
|  | LED4 | DAC\_D4 | T20 |  |  |
|  | LED5 | DAC\_D5 | R19 |  |  |
|  | LED6 | DAC\_D6 | P19 |  |  |
|  | LED7 | DAC\_D7 | U21 |  |  |
| 电平开关 | SW0 | | AA19 |  |  |
|  | SW1 | | V19 |  |  |
|  | SW2 | | V18 |  |  |
|  | SW3 | | Y19 |  |  |
|  | SW4 | | V20 |  |  |
|  | SW5 | | U20 |  |  |
|  | SW6 | | AB22 |  |  |
|  | SW7 | | AB21 |  |  |
| 开关 | KEY0 | | F4 |  |  |
|  | KEY1 | | C2 |  |  |
|  | KEY2 | | B2 |  |  |
|  | KEY3 | | E2 |  |  |
|  | KEY4 | | D2 |  |  |
|  | KEY5 | | U22 |  |  |
|  | KEY6 | | V22 |  |  |
|  | KEY7 | | T21 |  |  |
|  | KEY8 | | W20 |  |  |
| 数码管（不带译码） | Digitron\_Out\_0 | | N15 |  |  |
|  | Digitron\_Out\_1 | | R17 |  |  |
|  | Digitron\_Out\_2 | | P16 |  |  |
|  | Digitron\_Out\_3 | | N14 |  |  |
|  | Digitron\_Out\_4 | | N13 |  |  |
|  | Digitron\_Out\_5 | | R16 |  |  |
|  | Digitron\_Out\_6 | | P15 |  |  |
|  | Digitron\_Out\_7 | | P17 |  |  |
|  | DigitronCS\_Out\_0 | | R14 |  |  |
|  | DigitronCS\_Out\_1 | | R18 |  |  |
|  | DigitronCS\_Out\_2 | | T18 |  |  |
|  | DigitronCS\_Out\_3 | | N17 |  |  |
| 数码管（静态） | Digitron\_A\_0 | | P14 |  |  |
|  | Digitron\_A\_1 | | U18 |  |  |
|  | Digitron\_A\_2 | | U17 |  |  |
|  | Digitron\_A\_3 | | AB18 |  |  |
|  | Digitron\_B\_0 | | AA18 |  |  |
|  | Digitron\_B\_1 | | W17 |  |  |
|  | Digitron\_B\_2 | | V17 |  |  |
|  | Digitron\_B\_3 | | AB20 |  |  |
| XADC | AD15P | | J5 | 6 | JD1 引脚编号 |
|  | AD15N | | H5 | 8 | JD1 引脚编号 |
|  | AD7P | | K2 | 7 | JD1 引脚编号 |
|  | AD7N | | J2 | 9 | JD1 引脚编号 |
|  | AD14P | | H2 | 10 | JD1 引脚编号 |
|  | AD14N | | G2 | 12 | JD1 引脚编号 |
|  | AD6P | | K1 | 11 | JD1 引脚编号 |
|  | AD6N | | J1 | 13 | JD1 引脚编号 |
|  | AD5P | | E1 | 15 | JD1 引脚编号 |
|  | AD5N | | D1 | 17 | JD1 引脚编号 |
|  | AD13P | | G1 | 18 | JD1 引脚编号 |
|  | AD13N | | F1 | 20 | JD1 引脚编号 |
|  | AD4P | | B1 | 19 | JD1 引脚编号 |
|  | AD4N | | A1 | 21 | JD1 引脚编号 |
| DDR3L | DDR3L\_DQS0\_p | | Y3 |  |  |
|  | DDR3L\_DQS0\_n | | AA3 |  |  |
|  | DDR3L\_DQS1\_p | | R3 |  |  |
|  | DDR3L\_DQS1\_n | | R2 |  |  |
|  | DDR3L\_DQ0 | | V4 |  |  |
|  | DDR3L\_DQ1 | | AB2 |  |  |
|  | DDR3L\_DQ2 | | AB3 |  |  |
|  | DDR3L\_DQ3 | | AA1 |  |  |
|  | DDR3L\_DQ4 | | AA5 |  |  |
|  | DDR3L\_DQ5 | | Y4 |  |  |
|  | DDR3L\_DQ6 | | AB5 |  |  |
|  | DDR3L\_DQ7 | | AA4 |  |  |
|  | DDR3L\_DQ8 | | V2 |  |  |
|  | DDR3L\_DQ9 | | Y1 |  |  |
|  | DDR3L\_DQ10 | | U1 |  |  |
|  | DDR3L\_DQ11 | | Y2 |  |  |
|  | DDR3L\_DQ12 | | T1 |  |  |
|  | DDR3L\_DQ13 | | W1 |  |  |
|  | DDR3L\_DQ14 | | U2 |  |  |
|  | DDR3L\_DQ15 | | U3 |  |  |
|  | DDR3L\_A0 | | AA8 |  |  |
|  | DDR3L\_A1 | | U5 |  |  |
|  | DDR3L\_A2 | | Y9 |  |  |
|  | DDR3L\_A3 | | Y8 |  |  |
|  | DDR3L\_A4 | | V5 |  |  |
|  | DDR3L\_A5 | | W7 |  |  |
|  | DDR3L\_A6 | | U6 |  |  |
|  | DDR3L\_A7 | | V7 |  |  |
|  | DDR3L\_A8 | | T5 |  |  |
|  | DDR3L\_A9 | | W9 |  |  |
|  | DDR3L\_A10 | | AA6 |  |  |
|  | DDR3L\_A11 | | T6 |  |  |
|  | DDR3L\_A12 | | Y6 |  |  |
|  | DDR3L\_BA0 | | AB8 |  |  |
|  | DDR3L\_BA1 | | W5 |  |  |
|  | DDR3L\_BA2 | | Y7 |  |  |
|  | DDR3L\_DM0 | | AB1 |  |  |
|  | DDR3L\_DM1 | | W2 |  |  |
|  | DDR3L\_ODT | | AB6 |  |  |
|  | DDR3L\_CLK\_p | | V9 |  |  |
|  | DDR3L\_CLK\_n | | V8 |  |  |
|  | DDR3L\_CKE | | R4 |  |  |
|  | DDR3L\_CS | | U7 |  |  |
|  | DDR3L\_RAS | | AB7 |  |  |
|  | DDR3L\_WE | | W6 |  |  |
|  | DDR3L\_RESET | | T3 |  |  |
|  | DDR3L\_CAS | | T4 |  |  |